

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,954,773 B2
APPLICATION NO. : 09/966022
DATED : October 11, 2005
INVENTOR(S) : Jianwei Liu

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, line 29, "the adder" should read --the adder circuit--.
Col. 8, lines 36-37, "A and B outputs" should read --A and B signals--.
Col. 8, line 38, "X and Y" should read --the X and Y signals--.
Col. 8, line 40, "adder" should read --adder circuit-- and "X and Y" should read --the X and Y signals--.
Col. 8, line 50, "adder" should read --adder circuit-- and "X and Y" should read --the X and Y signals--.
Col. 8, line 60, "adder" should read --adder circuit--.
Col. 8, line 61, "adder" should read --adder circuit--.
Col. 8, line 63, "the X and Y outputs" should read --the X and Y signals--.
Col. 8, line 66, "adder" should read --adder circuit--.
Col. 9, line 25, "adder" should read --adder circuit--.
Col. 9, line 27, "adder" should read --adder circuit--.
Col. 9, line 29, "adder" should read --adder circuit--.
Col. 9, line 47, "adder" should read --adder circuit--.
Col. 9, line 49, "adder" should read --adder circuit--.
Col. 9, lines 55-56, "the first binary number" should read --a first binary number-- and "the second binary number" should read --a second binary number--.
Col. 9, lines 59-60, "the A and B outputs" should read --the A and B signals--.
Col. 9, line 62, "X and Y" should read --the X and Y signals--.
Col. 9, line 65, "the X and Y outputs" should read --the X and Y signals--.
Col. 9, line 66, "the input carry" should read --an input carry--.
Col. 10, line 1, "microprocessor circuit" should read --microprocessor--.
Col. 10, line 6, " microprocessor circuit" should read --microprocessor--.
Col. 10, line 12, " microprocessor circuit" should read --microprocessor--.
Col. 10, line 17, " microprocessor circuit" should read --microprocessor--.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 10, lines 43-44, "a first and a second binary number" should be --a first binary number and a second binary number--.

Signed and Sealed this
Eleventh Day of September, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office